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Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G-Circuits, Devices and Systems] , Volume: 146 , Issue: 4 , Aug. 1999

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2 A proposal for modeling substrate coupling in Si-MMICs and its experimental verification up to 40 GHz

Pfost, M.; Rein, H.-M.;

Microelectronic Test Structures, 1997. ICMTS 1997. Proceedings. IEEE International Conference on , 17-20 March 1997

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3 A semi-physical bipolar transistor model for the design of very-high-frequency analog ICs

Rein, H.-M.; Schroter, M.; Koldehoff, A.; Worner, K.;

Bipolar/BiCMOS Circuits and Technology Meeting, 1992., Proceedings of the 1992 , 7-8 Oct. 1992

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4 Investigation of very fast and high-current transients in digital bipolar IC's using both a new compact model and a device simulator

Schroter, M.; Rein, H.-M.;

Solid-State Circuits, IEEE Journal of , Volume: 30 , Issue: 5 , May 1995
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